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ingly, the resolution's speed trade-off is given by The curve labeled as $N = 1$ in Figure 14.11 illustrates this trade-off for As decreases, equation (14.14) shows that increases at the same rate.

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TRADE-OFFS IN ANALOG CIRCUIT DESIGN The Designer's Companion

Digital design is difficult enough on anybody's "good day." Combining analog circuits on a digital IC substrate is even more difficult. There will be performance trade-offs and sometimes the analog portions won't be as good as the application demands. This is where Maxim parts can help by replacing FPGA and ASIC functions with external devices.

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